

FIG. 1

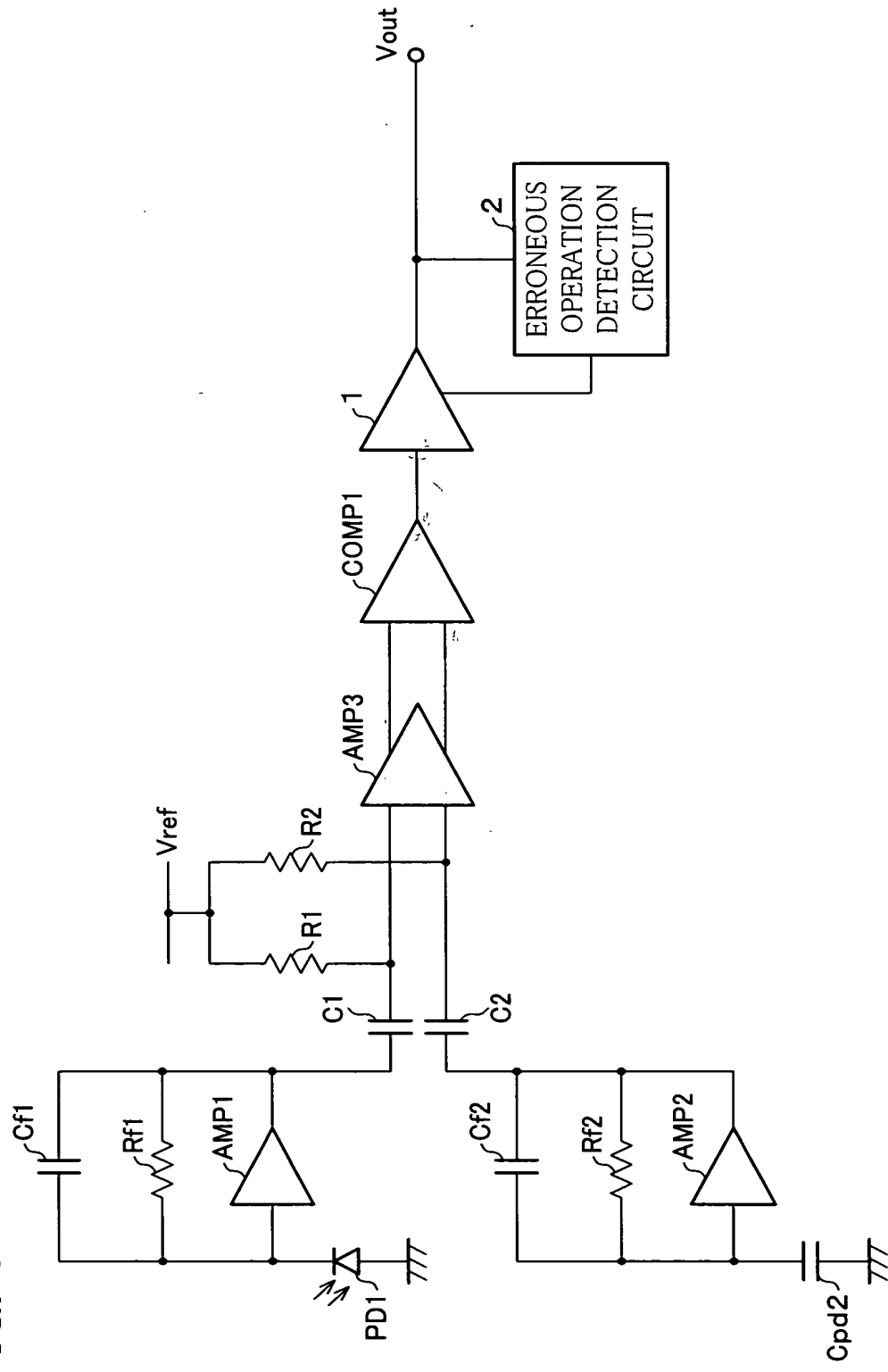


FIG. 2

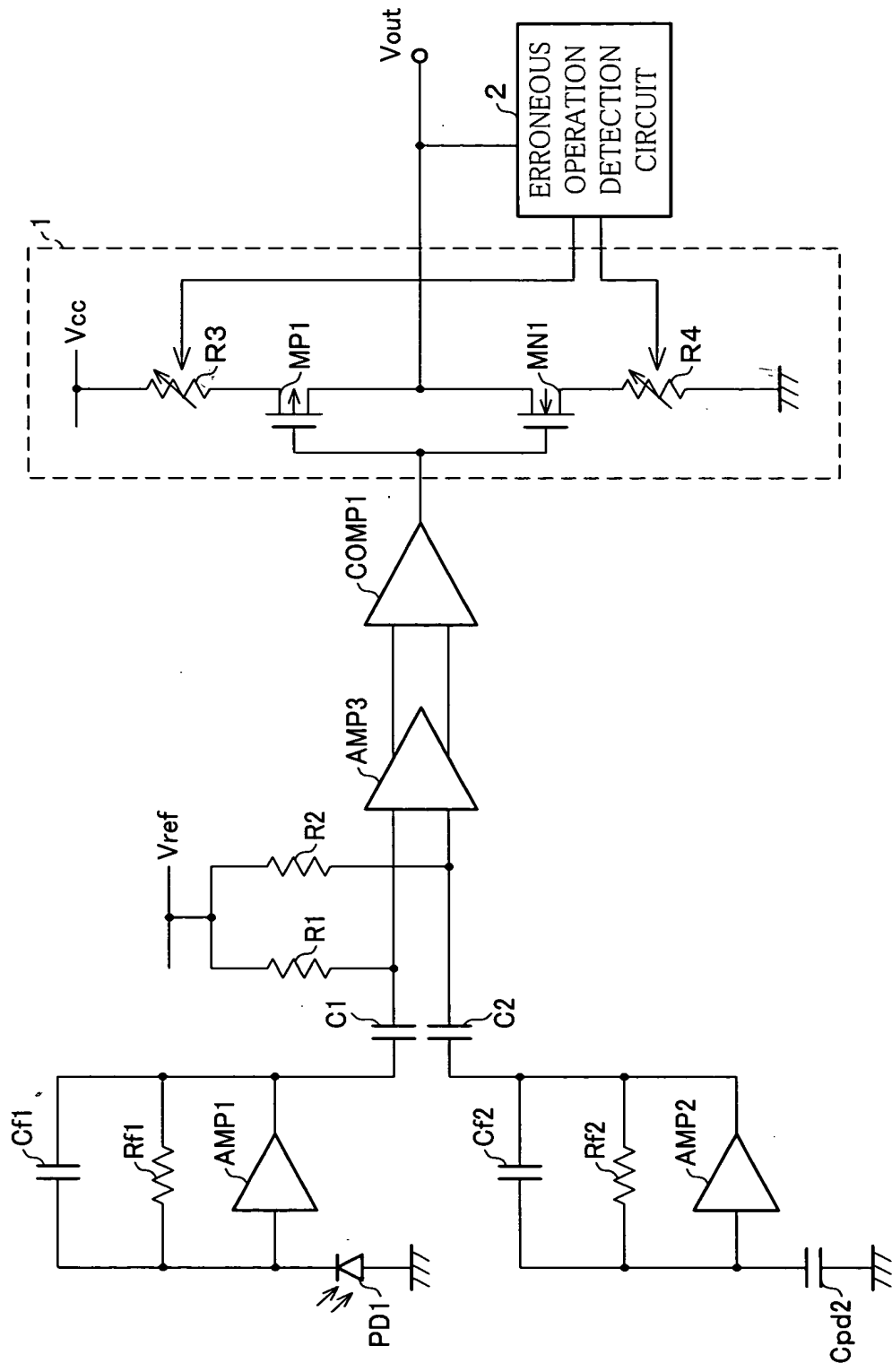


FIG. 3

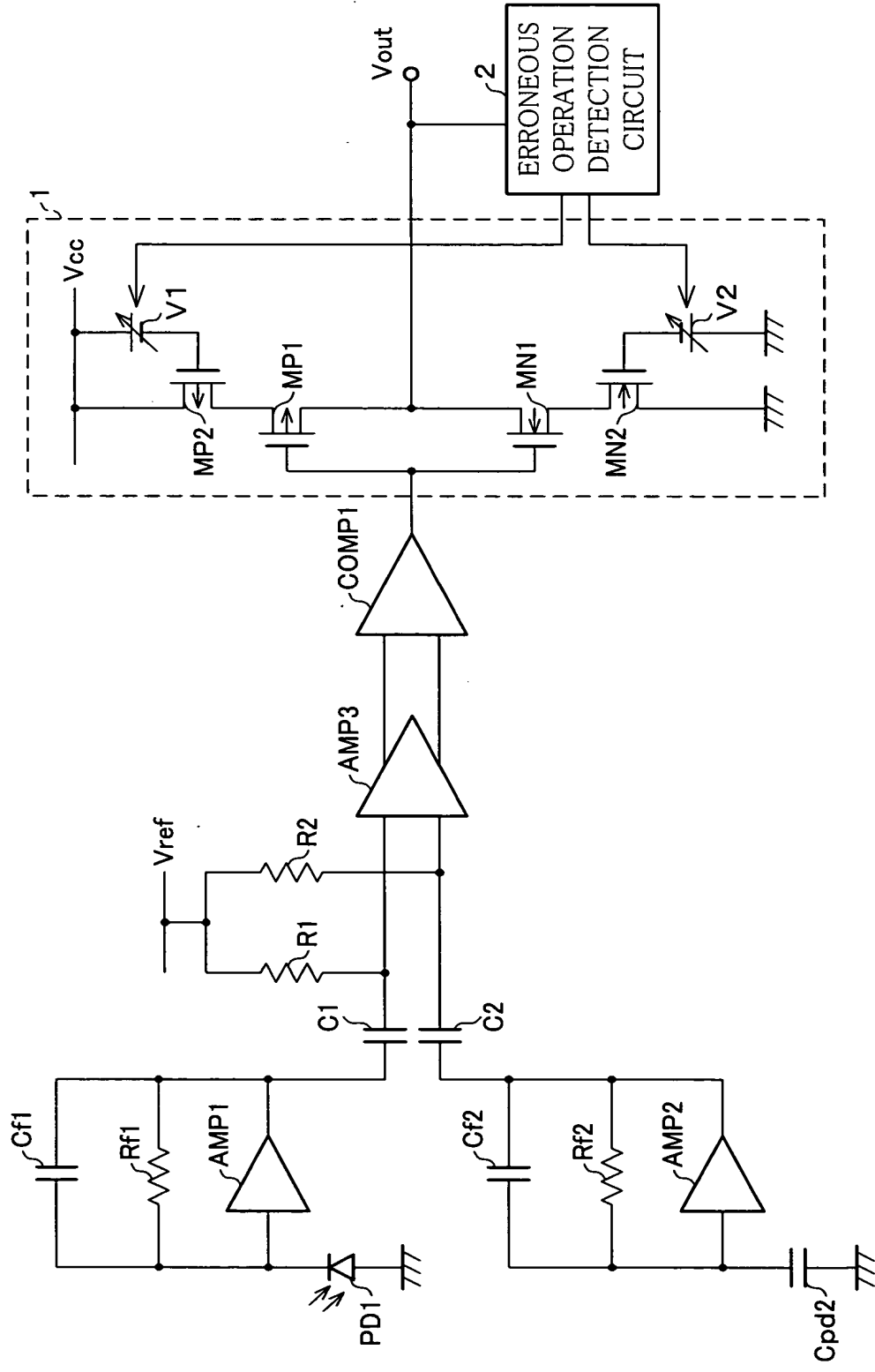


FIG. 4

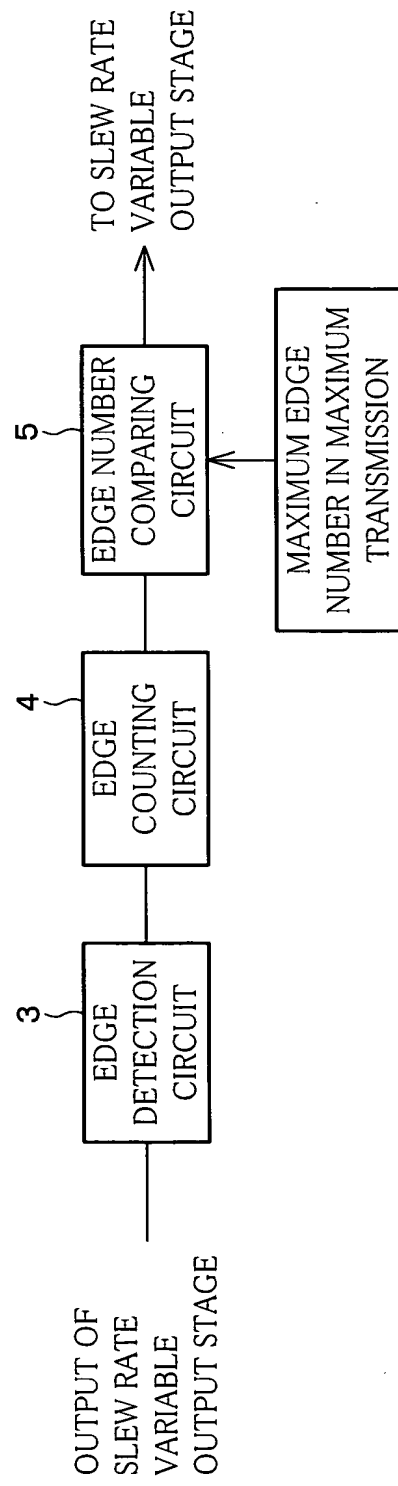
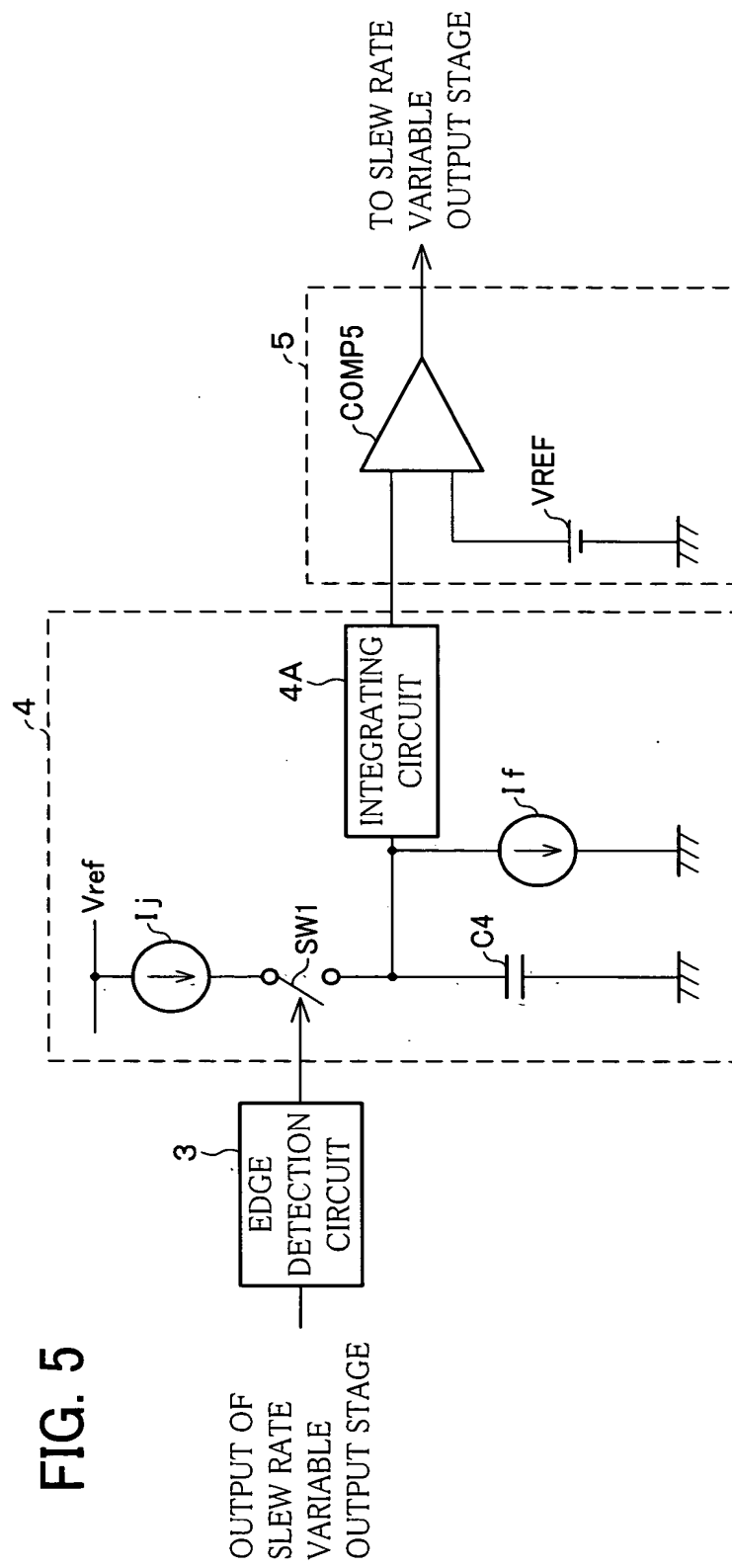


FIG. 5



IN CASE THERE IS NO
ERRONEOUS OPERATION IN
OUTPUT

FIG. 6 (a)

OUTPUT WAVEFORM



FIG. 6 (b)

OUTPUT WAVEFORM OF
CHARGING AND
DISCHARGING CIRCUIT

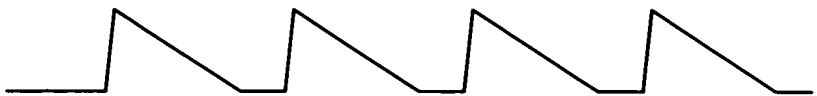
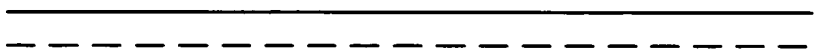


FIG. 6 (c)

OUTPUT WAVEFORM OF
INTEGRATING CIRCUIT



IN CASE THERE IS
ERRONEOUS OPERATION IN
OUTPUT

FIG. 6 (d)

OUTPUT WAVEFORM



FIG. 6 (e)

OUTPUT WAVEFORM OF
CHARGING AND
DISCHARGING CIRCUIT

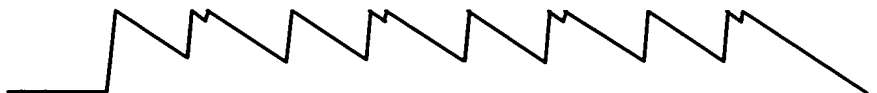
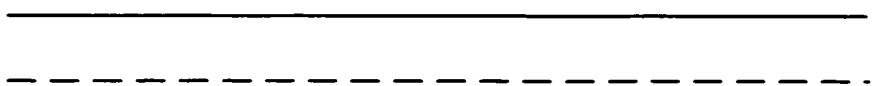


FIG. 6 (f)

OUTPUT WAVEFORM OF
INTEGRATING CIRCUIT



IN CASE THERE IS NO
ERRONEOUS OPERATION IN
OUTPUT

FIG. 7 (a)

OUTPUT WAVEFORM

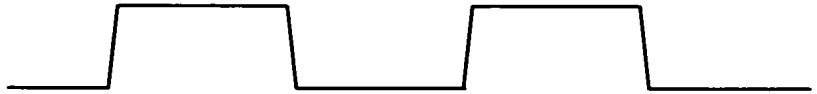


FIG. 7 (b)

OUTPUT WAVEFORM OF
CHARGING AND
DISCHARGING CIRCUIT

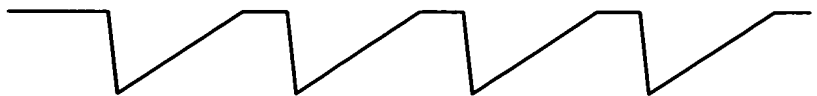
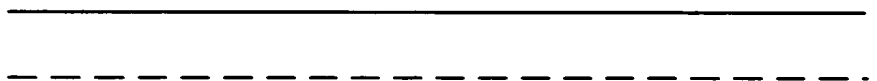


FIG. 7 (c)

OUTPUT WAVEFORM OF
INTEGRATING CIRCUIT



IN CASE THERE IS
ERRONEOUS OPERATION IN
OUTPUT

FIG. 7 (d)

OUTPUT WAVEFORM



FIG. 7 (e)

OUTPUT WAVEFORM OF
CHARGING AND
DISCHARGING CIRCUIT



FIG. 7 (f)

OUTPUT WAVEFORM OF
INTEGRATING CIRCUIT

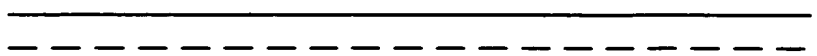


FIG. 8

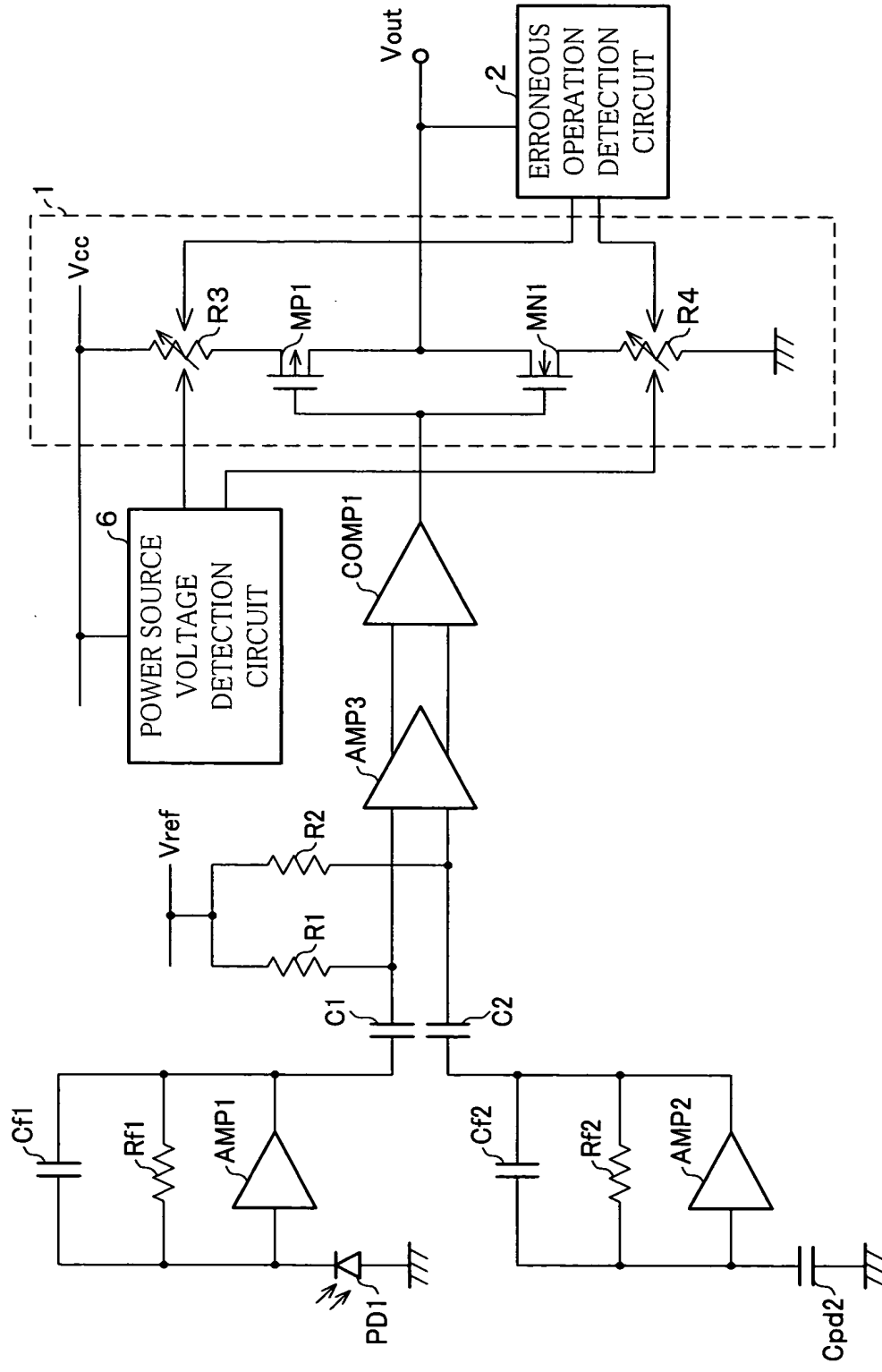


FIG. 9

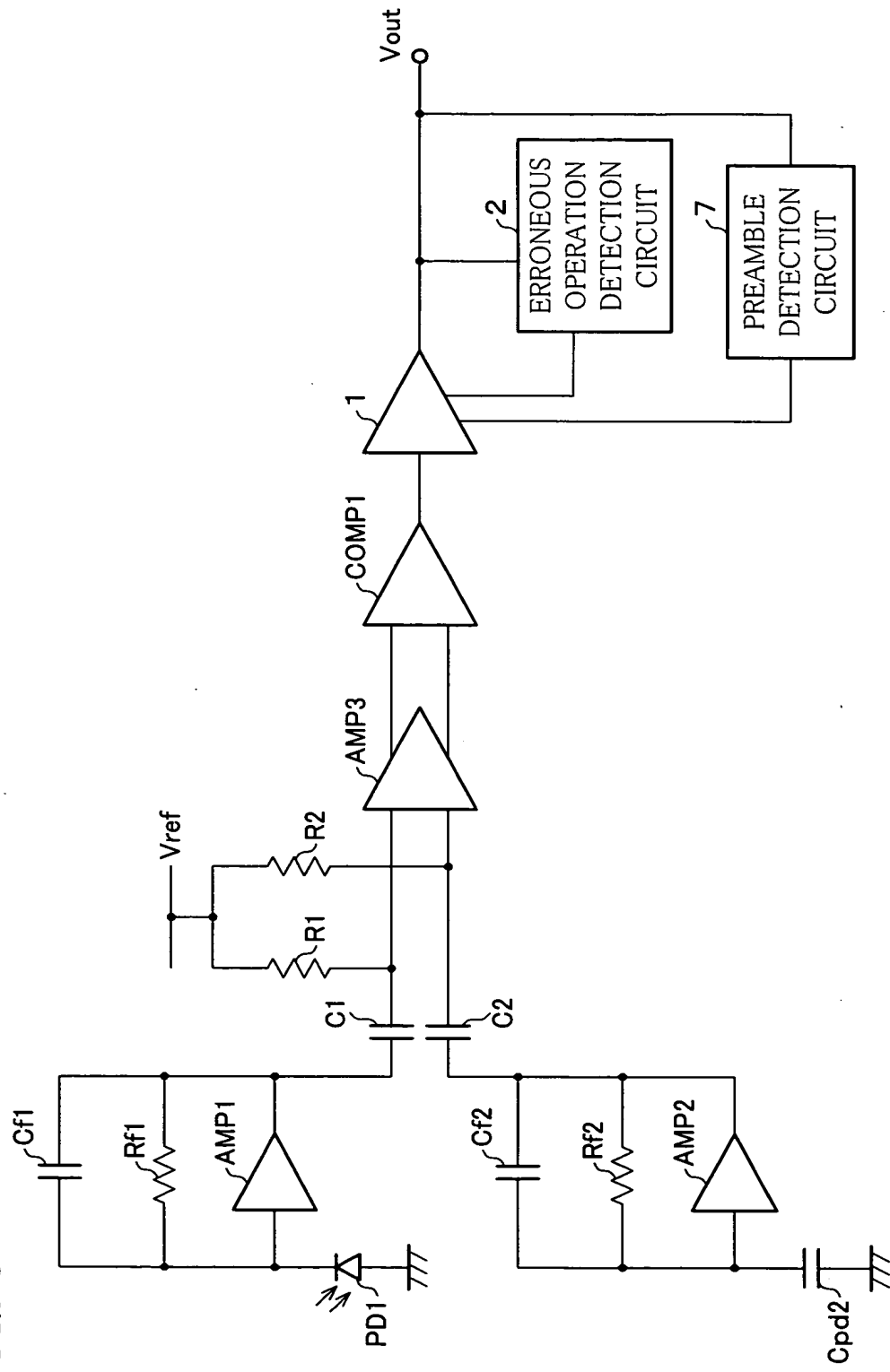


FIG. 10

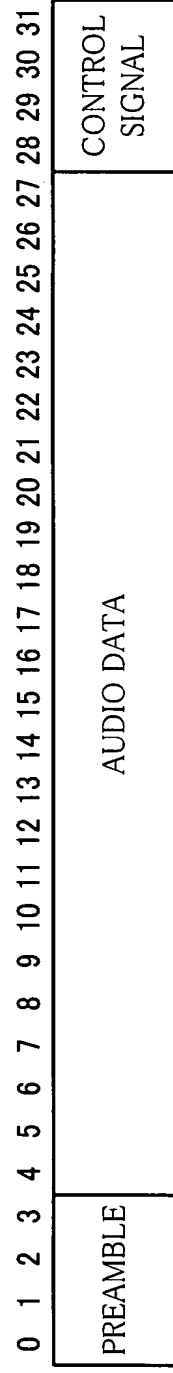


FIG. 11

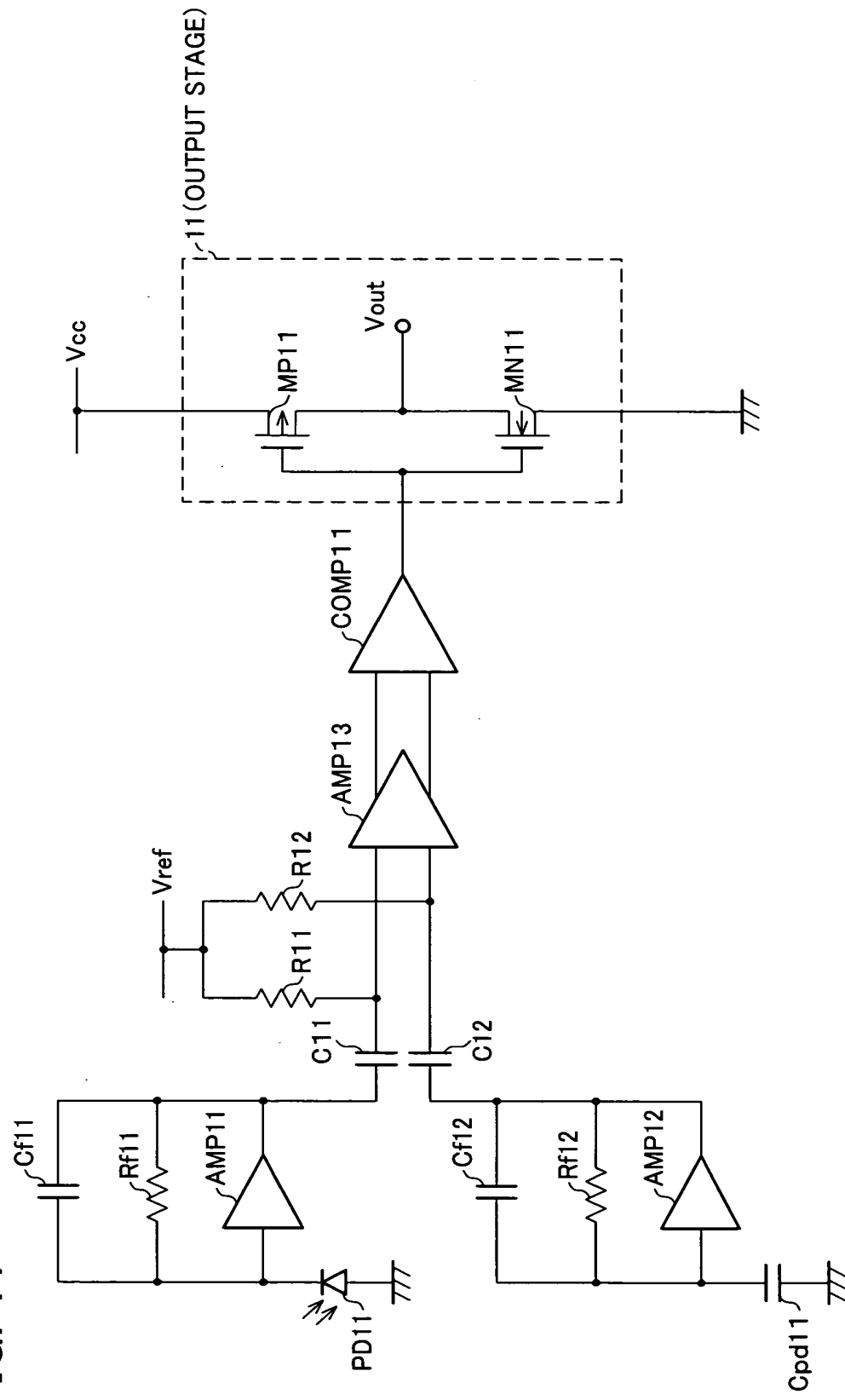


FIG. 12

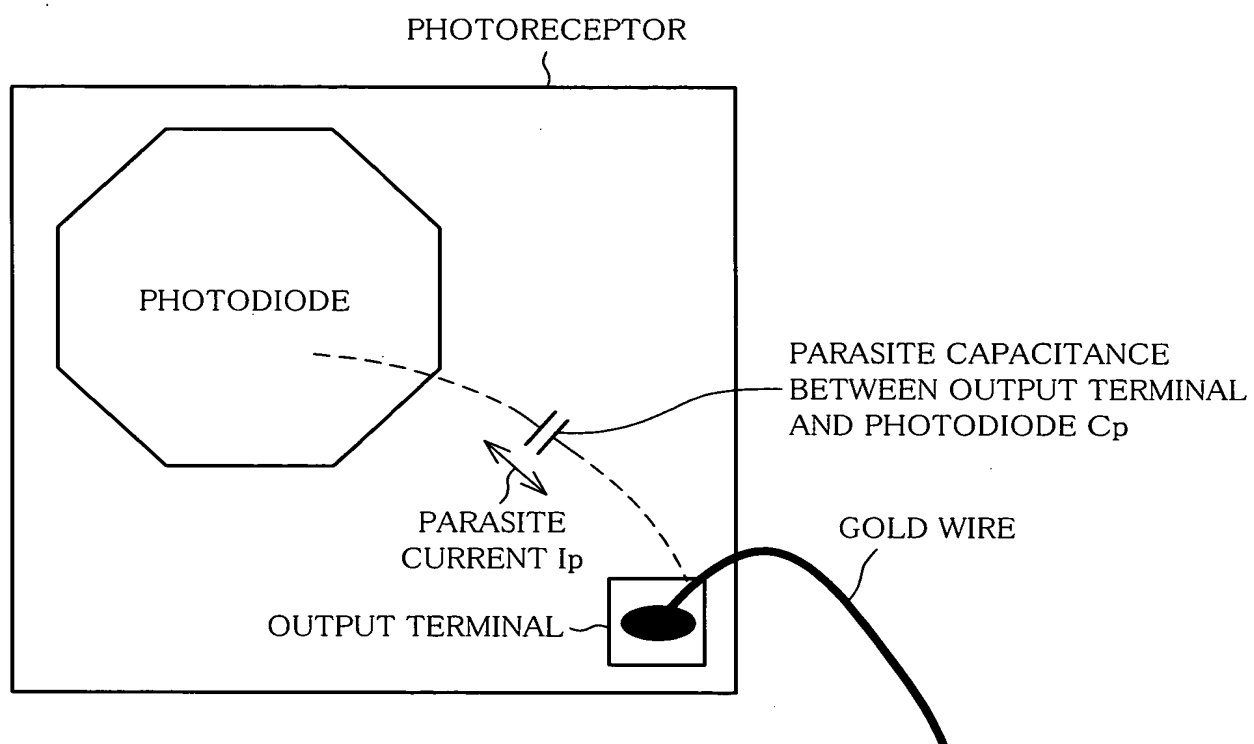


FIG. 13 (a)

OUTPUT WAVEFORM

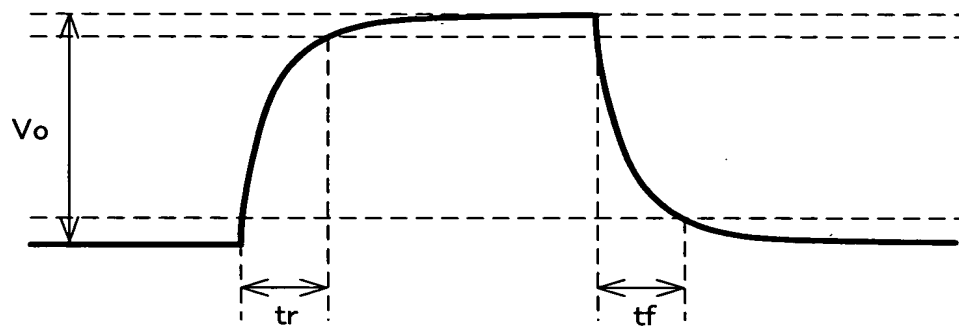


FIG. 13 (b)

WAVEFORM OF PARASITE CURRENT

